
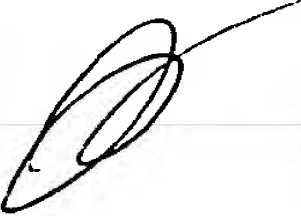

 <p>FORM PTO-1449</p> <p>INFORMATION DISCLOSURE STATEMENT</p>				ATTY. DOCKET NO. 1875.0220000		APPLICATION NO. 09/739,752	
				APPLICANT Chen et al.			
				FILING DATE December 20, 2000		GROUP 2811	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	5,886,392	03/1999	Schuegraf	257	530	
	AB1	6,044,012	03/2000	Rao et al.	365	182	
	AC1						
	AD1						
	AE1						
	AF1						
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AG1						Yes No
	AH1						Yes No
	AI1						Yes No
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
	AJ	1	Clark, Lawrence T., "A High-Voltage Output Buffer Fabricated on a 2V CMOS Technology," <i>Symposium on VLSI Circuits Digest of Technical Papers</i> , pp. 61-62, (1999).				
	AK	1					
	AL	1					
	AM	1					
	AN	1					
EXAMINER				DATE CONSIDERED 2/11/02			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.